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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/870,253	05/30/2001	Jong-Hong Bae	29926/36988	8837

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EXAMINER

KIK, PHALLAKA

ART UNIT

PAPER NUMBER

2825

DATE MAILED: 08/05/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/870,253

Applicant(s)

BAE ET AL.

Examiner

Phallaka Kik

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-9 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 30 May 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____.
- 4) ☐ Interview Summary (PTO-413) Paper No(s) ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Claim Objections

2. **Claims 3,5-9** are objected to because of the following informalities:

As per **claim 3**, "claim 1" (line 1) should be --claim 2-- to provide proper antecedent basis for "the multiplexer" (line 1); "multiplexer" (line 1) should be --set of multiplexers-- for proper antecedent basis; "the plurality" (line 6) should be --a plurality-- for proper antecedent basis.

As per **claim 5**, --of the second multiplexer-- should be inserted after "terminal" (lines 9 and 10) to clearly differentiate from the first and second input terminals of the first multiplexer.

As per **claim 6**, "fictional" (line 3) should be --functional--; "a program codes" (line 4) should be --program codes-- since "codes" is plural; "the program" (line 4) should be --a program-- for proper antecedent basis.

As per **claim 7**, "the RAM" (line 5) should be --RAM-- for proper antecedent basis; "the SFR" (line 5) should be --SFR-- for proper antecedent basis; --of the second multiplexer-- should be inserted after "terminal" (lines 9 and 10).

As per **claim 9**, "the" (line 3) should be deleted for proper antecedent basis; "decoded" (line 5) should be --coded-- for proper antecedent basis since "coded" was

used in claim 6 (line 11) , from the claim depends; "the first" (line 6) should be --a first-- for proper antecedent basis.

As per **claims 7-9**, the claims are also objected to for incorporating the above errors into the respective claims by claim dependency.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. **Claims 1-9** are rejected under 35 U.S.C. 102(b) as being anticipated by **Butts et al.** (US Patent No. 5,796,623).

Butts et al. disclose an apparatus and method for performing computations, prototyping, execution, simulation using electrically reconfigurable gate arrays (ERCGA) logic chips (abstract).

As per **claims 1,2,4,6,8-9**, all of the elements of the claims are illustrated in Fig. 1, wherein the target board corresponds to one of the boards in the Realizer Hardware System (see col. 7, lines 44-46), the MUP chip corresponds to the Host Computer (see Figs. 49, 54-57 for interactions of the host computer to generate the necessary signals/commands/programs to configure/re-configure/analyze/simulate the circuit using the Realizer Hardware System), the storage blocks (or receiving means), including

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RAMS and register addresses and data are further described in col. 24, line 10 to col. 34, line 53, wherein the decoder and multiplexer associated with data and address accesses are further described in col. 27, line 26 to col. 28, line 6 (see also col. 33, lines 1-20); wherein the multiplicity of I/O ports associated with the communication means are part of the host interface (col. 7, lines 38-40).

As per **claim 3**, the multiplexers including a three-phase buffer (i.e., tri-state buffers) and a second or plurality of multiplexer(s) are further described in col. 33, lines 1-20.

As per **claims 5,7**, the particular interactions of the i/o ports in conjunction with the multiplexers, RAM or register data and addresses, are further illustrated in Figs. 24-27, 32-33.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Therefore, Applicant is requested to consider them carefully in response to this Office Action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phallaka Kik whose telephone number is 703-306-3039. The examiner can normally be reached on Flexitime.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 703-308-1323. The fax phone numbers for the organization where this application or proceeding is assigned are 703-

872-9318 for regular communications and 703-872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-1782.

Any response to this action should be mailed to:

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

or faxed to:

703-872-9318 (for Before-Final) and 703-872-9319 (for After-Final) for formal communications intended for entry,

Or:

(703) 746-4111 (for informal or draft communications, please label "PROPOSED" or "DRAFT" and let the examiner know prior to faxing)
Hand-delivered responses should be brought to Crystal Plaza 4, 2201 South Clark Place, Arlington, VA 22202, Fourth Floor (Receptionist).

7. Applicant should note that effective May 1, 2003, the United States Patent and Trademark Office has a new Commissioner for Patents address for transitioning to the new Office location in Alexandria, VA, wherein correspondence in patent-related matters to organizations reporting to the Commissioner for Patents must now be addressed to:

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PK 
July 19, 2003



MATTHEW SMITH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800